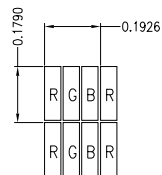
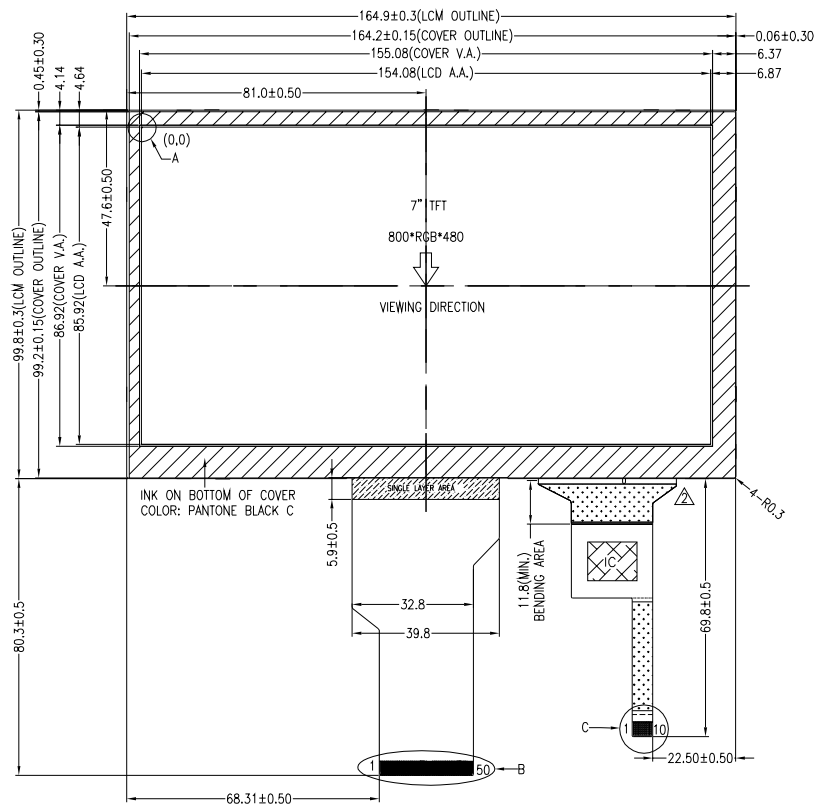
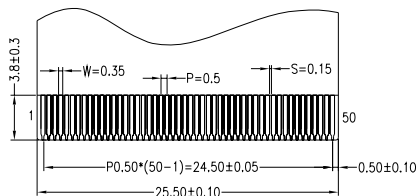


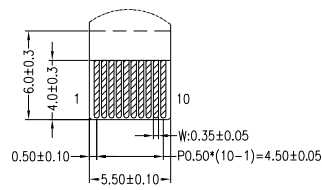
NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



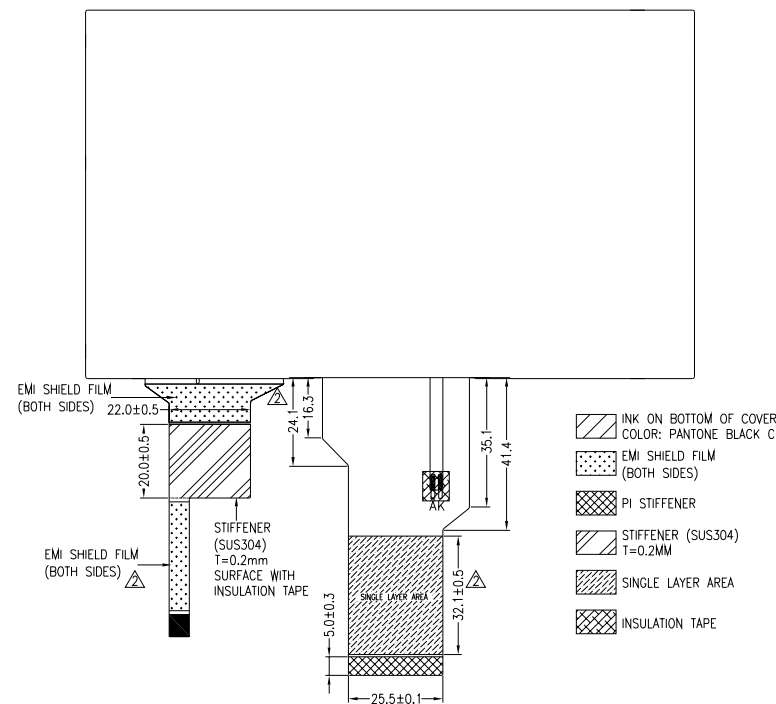
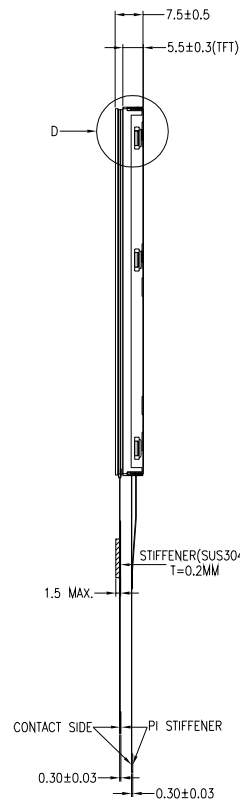
DETAIL A
(SCALE 40:1)



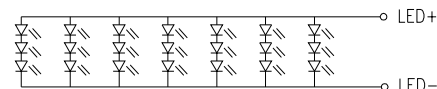
DETAIL B
(SCALE 4:1)



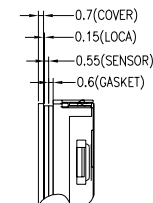
DETAIL C
(SCALE 4:1)



BACKLIGHT CIRCUIT DIGRAM



DETAIL D
(SCALE 2:1)



PIN ASSIGNMENT FOR LCM	
NO.	SYMBOL
1	LED+
2	LED+
3	LED-
4	LED-
5	GND
6	VCOM
7	DVDD
8	MODE
9	DE
10	VS
11	HS
12	B7
13	B6
14	B5
15	B4
16	B3
17	B2
18	B1
19	B0
20	G7
21	G6
22	G5
23	G4
24	G3
25	G2
26	G1
27	G0
28	R7
29	R6
30	R5
31	R4
32	R3
33	R2
34	R1
35	R0
36	GND
37	DCLK
38	GND
39	L/R
40	U/D
41	VGH
42	VGL
43	AVDD
44	RESET
45	NC
46	VCOM
47	DITHB
48	GND
49	NC
50	NC

PIN ASSIGNMENT FOR CTP	
NO.	SYMBOL
1	VSS
2	VCC
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: DVD=3.3V (TYP.)
3. VIEWING DIRECTION: 12:00 O'CLOCK
4. DRIVER IC: HY264-E00+HX8664-B00 OR EK9713CA+EK73002ACGB
5. LED BACKLIGHT: 21-CHIP WHITE LED, If=140mA, Vf=9.6V±1.2V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
7. RECOMMEND MATCHING CONNECTOR: HRS FH28-50S-0.5SH OR EQUIVALENT

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 340 CD/M² (TYP.) (WITH CTP)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

1. 7.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VCC=IOVCC=3.3V
4. DRIVER IC: FT7311DQ, INTERFACE: IIC
5. RESOLUTION: 800*480 DOTS
6. MULTI FINGER: UP TO 5
7. HARDNESS OF COVER SURFACE: 7H (500g)
8. RECOMMEND MATCHING CONTROLLER: HRS FH28-10S-0.5SH OR EQUIVALENT
9. FIRMWARE FILE: MI0700CSP-CZA_FIRMWARE_V0B_20240507_ALL.BIN

02	ADD FIRMWARE VERSION OF CTP CHANGE THE SHAPE OF CTP FPC ADD EMI SHIELD FILM FOR CTP FPC CHANGE THE SINGLE LAYER AREA SIZE OF CTP FPC	RITA	2024.06.19	DRAWN BY: RITA	2023.12.05		MODULE P/N: MI0700S6T-1CP1A
01	FIRST ISSUE	CAROL	2023.12.05	CHECKED BY:		SCALE : 1/1	
VER.	REVISED DESCRIPTION	REVISER	DATE	APPROVED BY:		UNIT: mm	DESCRIPTION:
				VERSION NO:	02	SHEET NO.: 1/1	TFT + CTP MODULE